IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of

DAVID L O'MEARA, et al.

Group Art Unit: 2812

Application No.: 10/630,970

Examiner:

Filed: July 31, 2003

Confirmation No.: 1845

For: METHOD OF FORMING UNIFORM ULTRA-THIN OXYNITRIDE LAYERS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

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Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449s. As this application was filed after June 30, 2003, no copies of the cited U.S. patent or patent publication are attached. However, one copy of each non-patent literature reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

Respectfully Schmitted

Dale S. Lazar

Registration Number 28872

Date: January 9, 2004

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PTO/SB/08a (08-03)

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300	suitate for form 1443/VI TO		Application Number	10/630,970		
11	NFORMATION DISC	CLOSURE	Filing Date	07/31/2003		
	STATEMENT BY AP		First Named Inventor	DAVID L O'MEARA		
3	MAILMENT DI AI	LIOAN	Art Unit	2812		
	(Use as many sheets a <u>s nece</u>	essarv)	Examiner Name			
Sheet		of	Attorney Docket Number	071469-0303786		

				U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	Number-Kind Code ^{2 (if k} US- 6 107 641	B1	03-06-2001	HERGENROTHER et al.	
	1	US- 6,197,641	- BI			
	2	6,090,723		07-18-2000	THAKUR et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449B/PTO Complete if Known Application Number 10/630,970 INFORMATION DISCLOSURE Filing Date 07/31/2003 STATEMENT BY APPLICANT First Named Inventor DAVID L O'MEARA Art Unit 2812 (Use as many sheets as necessary) Examiner Name Attorney Docket Number 071469-0303786 Sheet

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
	A	LIU, H. I. ET AL., "Self-limiting oxidation of Si nanowires," J. Vac. Sci. Technol. B, American Vacuum Society, Vol. 11 (No. 6), p. 2532-2537, (Nov/Dec 1993).				
	В	LIU, H. I. ET AL., "Self-limiting oxidation for fabricating sub-5 nm silicon nanowires," Appl. Phys. Letter, American Institute of Physics, Vol. 64 (No. 11), p. 1383-1385, (March 14, 1994).				
	С	FUKUDA, HIROSHI ET AL., "Fabrication of silicon nanopillars containing polycrystalline silicon/insulator multilayer structures," Appl. Phys. Lett., American Institute of Physics, Vol. 70 (No. 3), p. 333-335, (January 20, 1997).				
ā.	D	ROY, P. K. ET AL., "Gate Dielectrics," Bell Labs, p. 276-289.				
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Examiner	Date	
Signature	Considered	

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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